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S/N 10/521908

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA	Examiner:	HOANG, Tu Ba
Serial No.:	10/521908	Group Art Unit:	2832
Filed:	January 20, 2005	Docket No.:	10921.0269USWO
Title:	CHIP RESISTOR AND MANUFACTURING METHOD THEREFOR		

CERTIFICATE UNDER 37 CFR 1.6(d)

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on December 15, 2006.

By: 

Name: Lauren Stindt

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants elect Group I (claims 1 to 6) in response to the Restriction
Requirement of November 16, 2006.

Please charge any additional fees or credit any overpayment to Deposit
Account No. 50-3478.

Respectfully submitted,

52835

Hamre, Schumann,
Mueller & Larson, P.C.
P.O. Box 2902-0902
Minneapolis, MN 55402
Phone: 612-445-3800

Date: December 15, 2006By: 

Name: Douglas P. Mueller
Reg. No. 30,300
DPM/lis